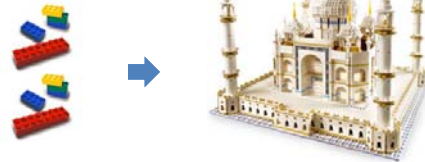


# Compositional Model-Based System Design

Stavros Tripakis

Aalto University and UC Berkeley



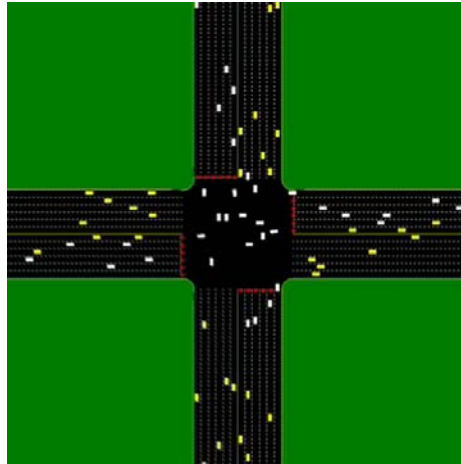
CRTS 2015, San Antonio, TX  
December 1, 2015

## The computer-controlled society



## Example of a “smart” system (or CPS): autonomous intersection

Courtesy AIM project,  
CS Dept., UT Austin



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## Autonomous intersection: “real-life” version



Courtesy <http://www.fastcodesign.com>

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Thanks to Christos Cassandras for recommending this video

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# How to design safety-critical systems?

- **Trial and error**

- Un-scalable
- Un-economic
- Un-safe
- Yet common...

*Are the drivers supposed to debug the autopilot?*

“It was described as a **beta release**. The system **will learn over time** and get better and that’s exactly what it’s doing. It will start to feel quite refined within **a couple of months**.” – Elon Musk, Tesla CEO

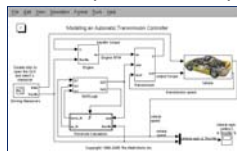
Tesla autopilot video (source: youtube)



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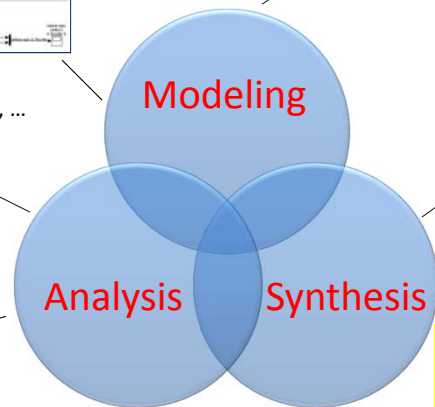
# A better approach: Model-Based Design

Simulink, UML, SysML, HDLs, SystemC, ...



Simulation, verification, ...

Be sure that this is what we want

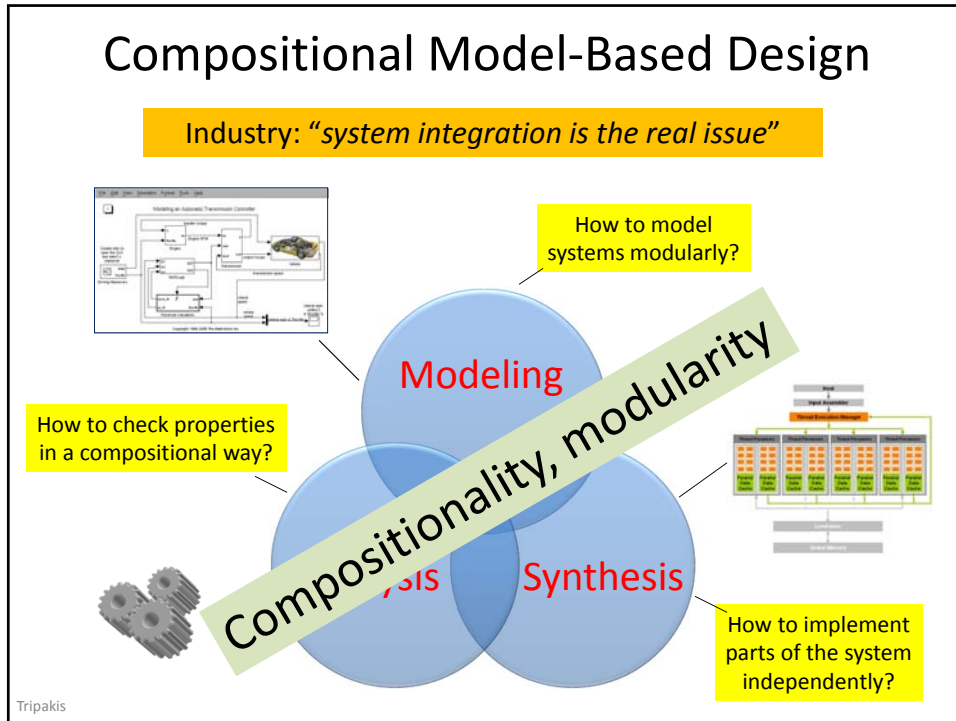


Describe the system that we want



Implement the system Automatically  
Correct-by-construction

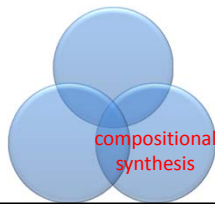
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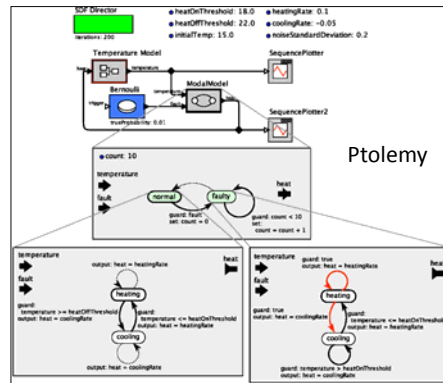
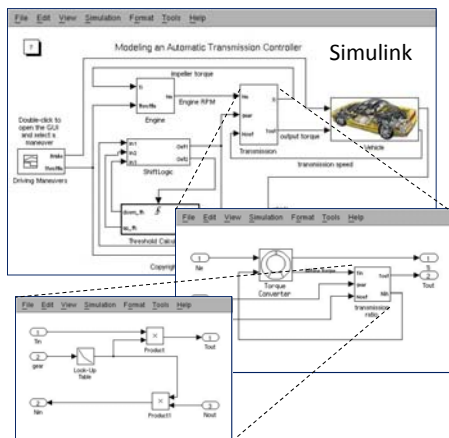
- ## My work in the past 7 years
- Modeling:
    - Achieving compositionality in hierarchical models
    - Multi-view modeling
  - Analysis:
    - Incremental verification using refinement theories
    - Composition of heterogeneous formalisms
  - Synthesis/implementation:
    - Modular code generation
- Common theme: interfaces**
- Disclaimer:  
this talk is NOT a survey of  
compositionality/interfaces*
- Tripakis 8

# Modular code generation and compositionality in hierarchical modeling languages

Joint work with R. Lubliner, C. Szegedy, E. Lee, M. Geilen, B. Rodiers, D. Bui

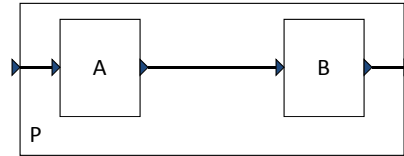


## Hierarchy



model = tree of sub-models

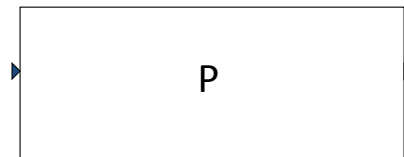
## Hierarchy in block diagrams



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## Hierarchy in block diagrams



**Modularization:  
hide details, master complexity**

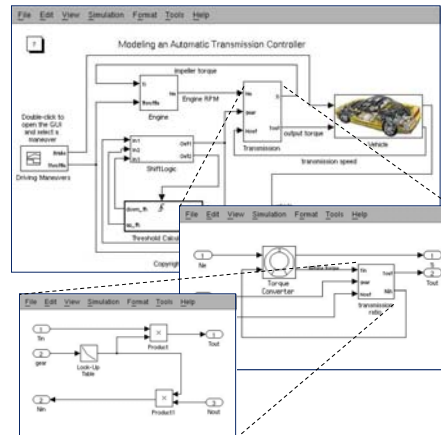
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## Hierarchy benefits

Total number of blocks: ~100

Max. number at any level: ~6



model = tree of sub-models

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## Can we exploit this hierarchy beyond syntax?

- Can we reuse a submodel? } c.f. software reuse
- Can we treat it as a “black box”? } c.f. software components
- Can we build model libraries? } c.f. software libraries

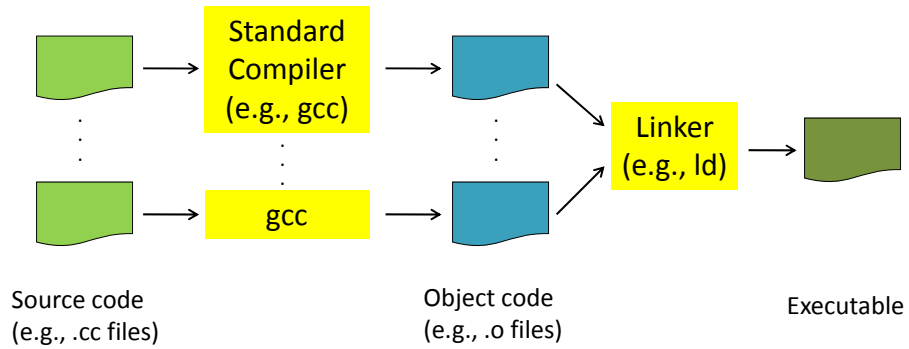
Answer: **no**, in most state-of-the-art languages and tools

These languages are fundamentally **non-compositional**  
(the composition of two or more components is not always a component)

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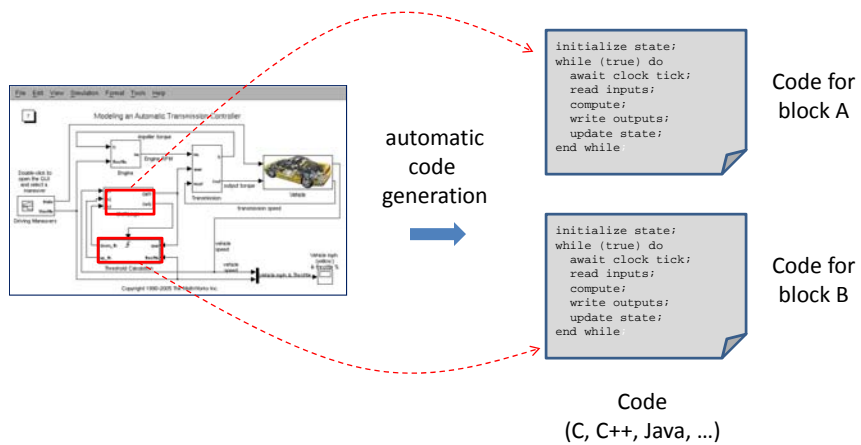
## Example: suppose we want to do modular compilation



**Enables incremental compilation, IP protection, ..., and libraries!**

## Modular code generation

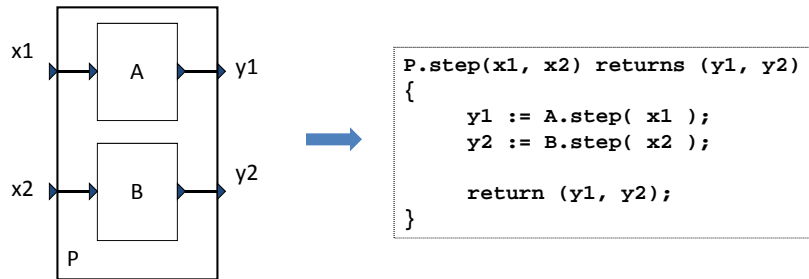
**Can we do the same for model-based design languages?**





## The standard approach

- “**Monolithic**” code generation

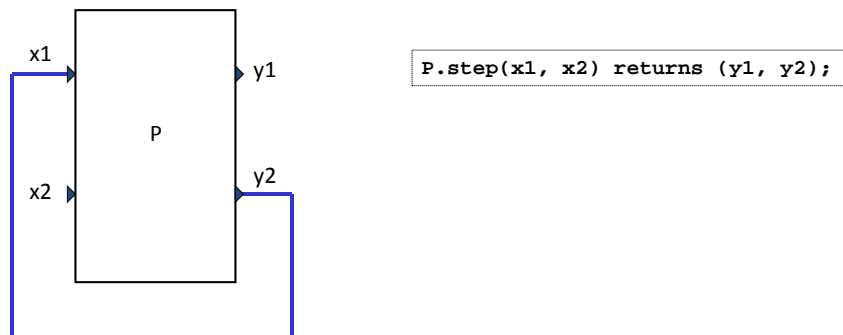


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## The standard approach

- Problem with monolithic code



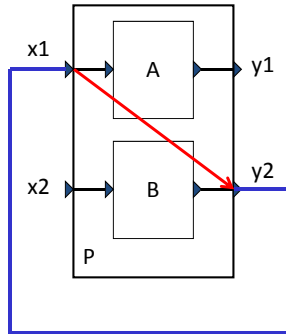
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# Non-compositionality of hierarchical block diagrams

False I/O dependencies  
=>  
code not usable in some contexts

Parallel composition of functions is not a function!

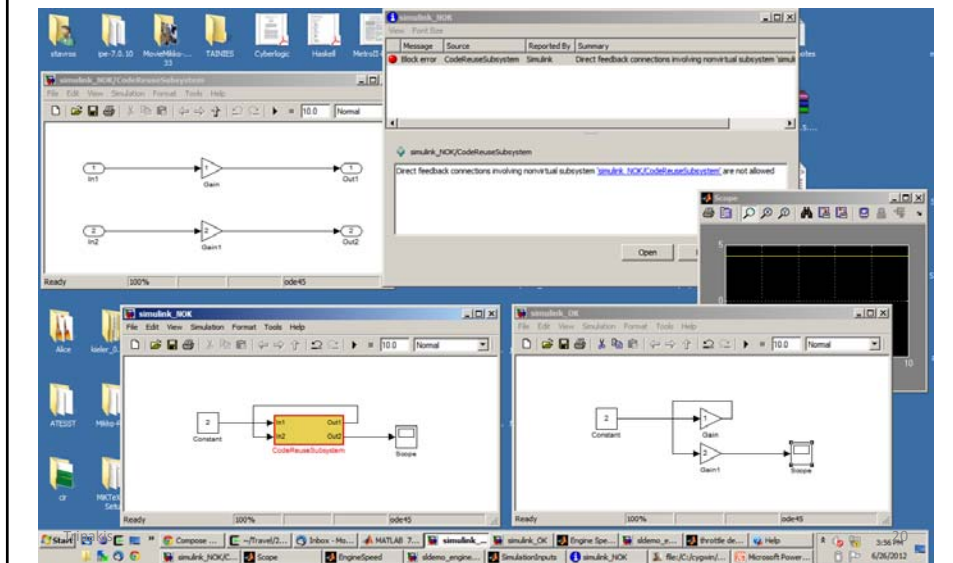


`P.step(x1, x2)` returns `(y1, y2);`

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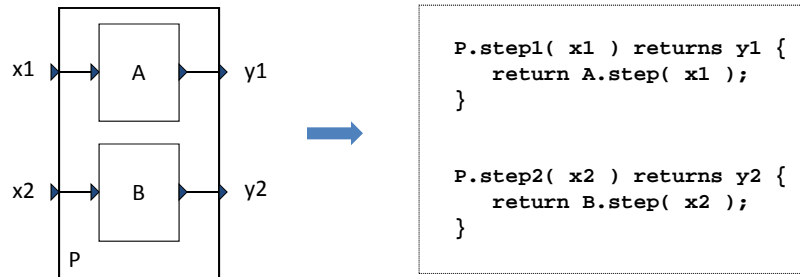
# State-of-the-art tools (e.g., Simulink) suffer from this problem



[DATE'08, POPL'09]

## Solution: non-monolithic code

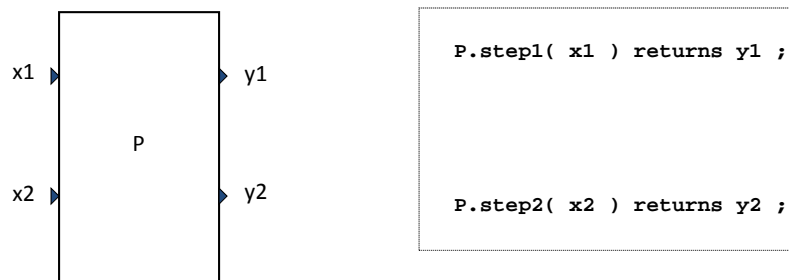
A state machine (Mealy) with multiple output functions



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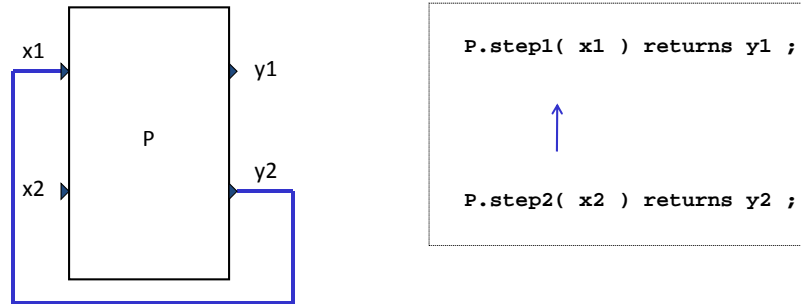
## Non-monolithic interface



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## Non-monolithic interface

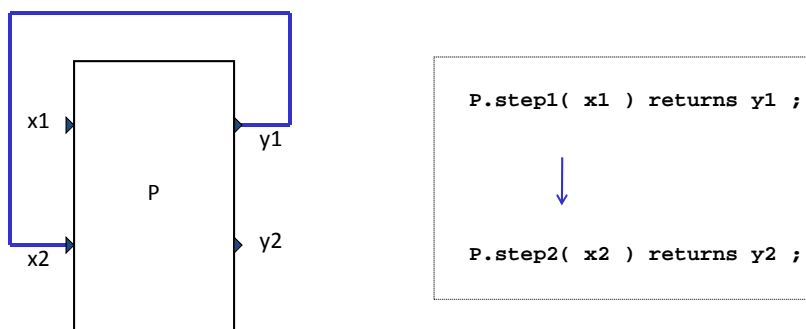


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## Non-monolithic interface

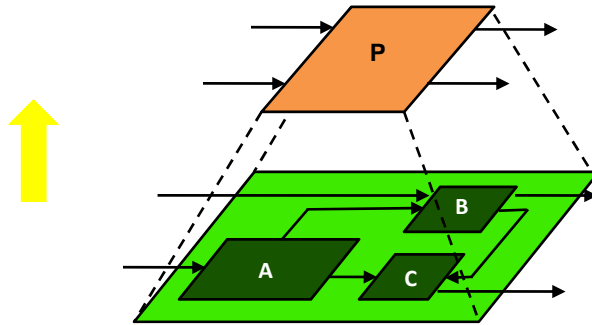
**interface does not restrict usage**



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## Bottom-up interface synthesis

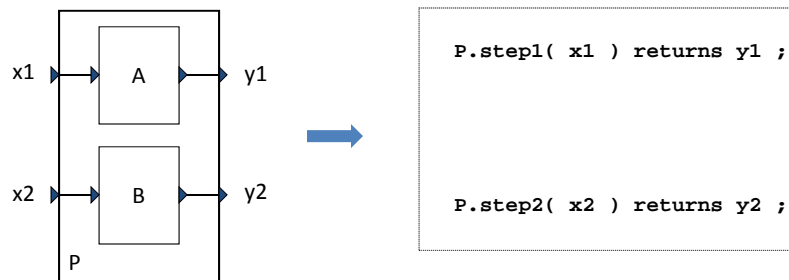


Given interfaces for sub-blocks A, B, C, compute interface for composite block P.

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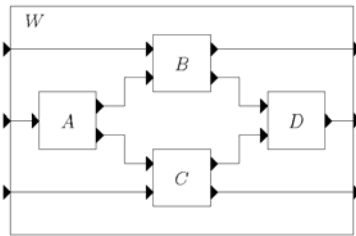
## Interface synthesis: sometimes relatively simple ...



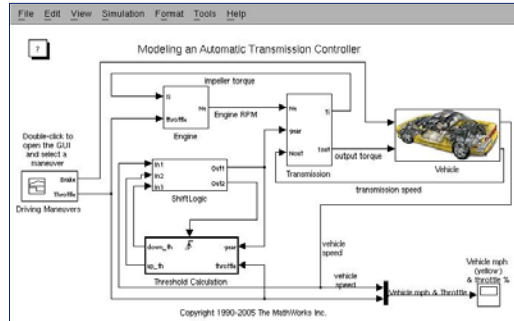
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... but generally non-trivial



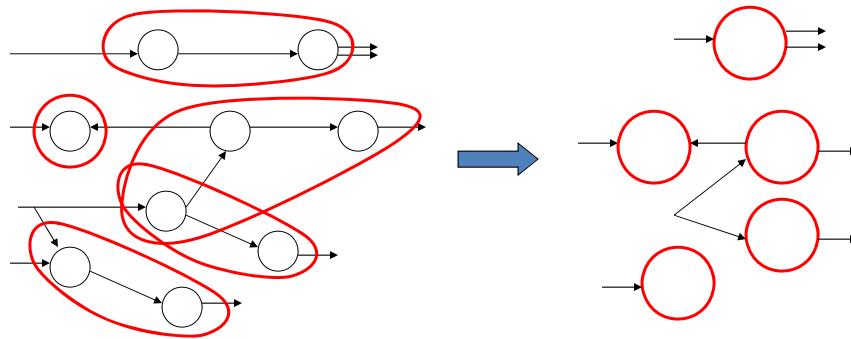
what about this?



or this?

Skip details

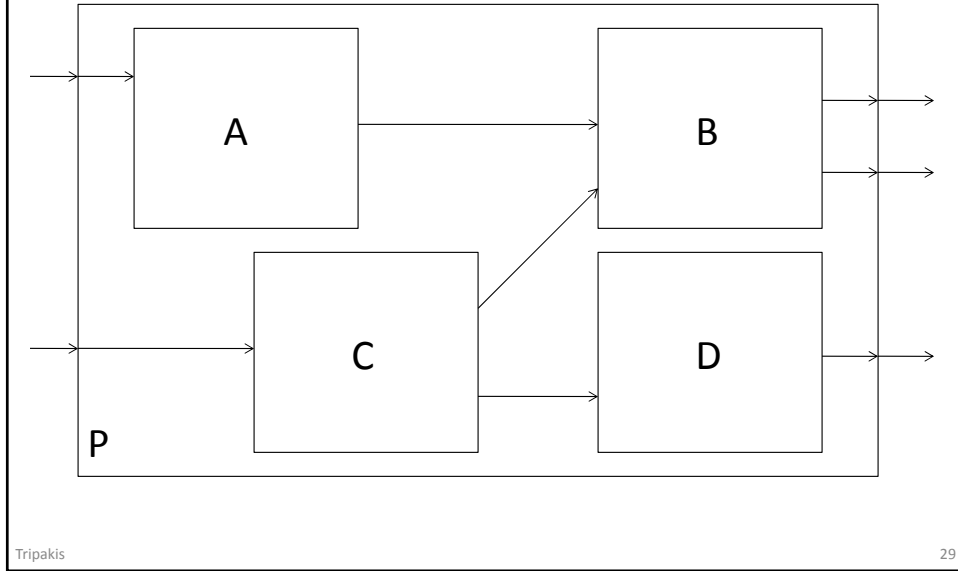
Interface synthesis for block diagrams  
= graph clustering



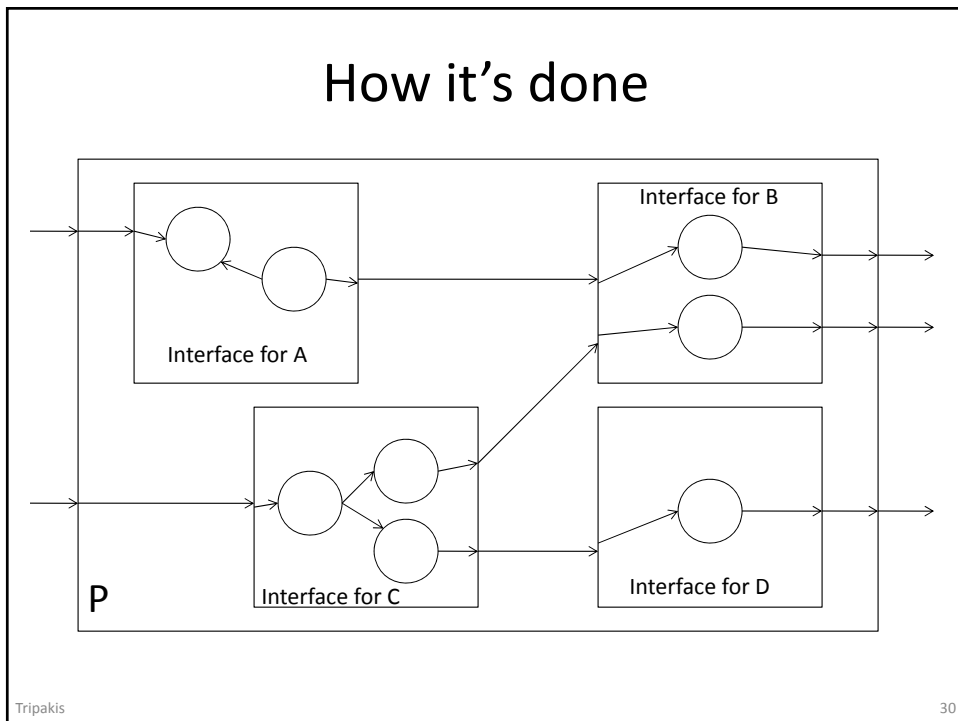
block diagram

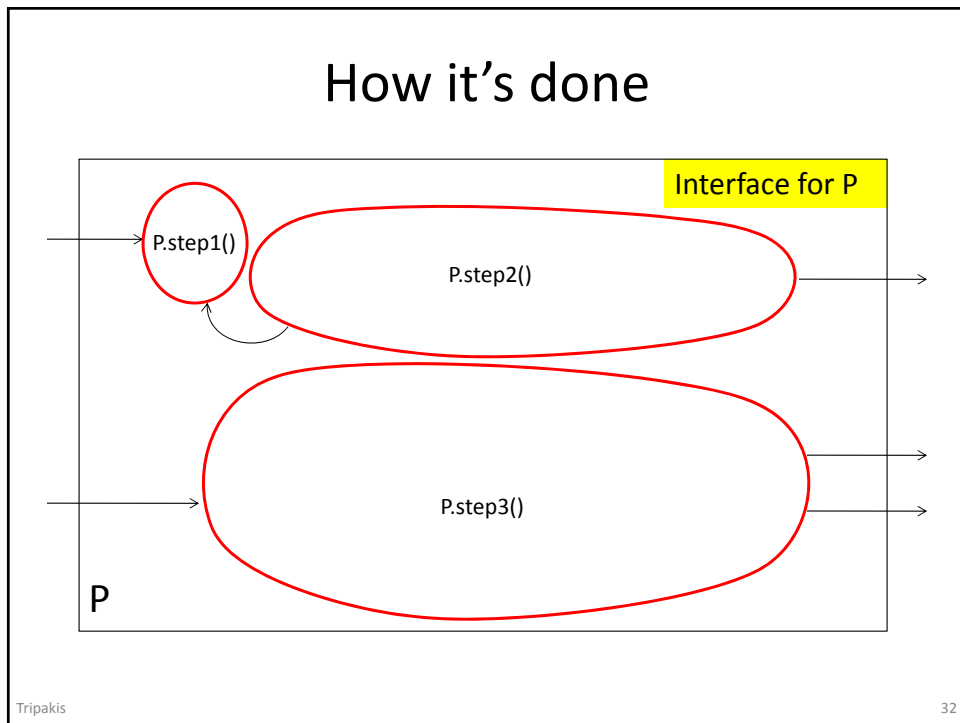
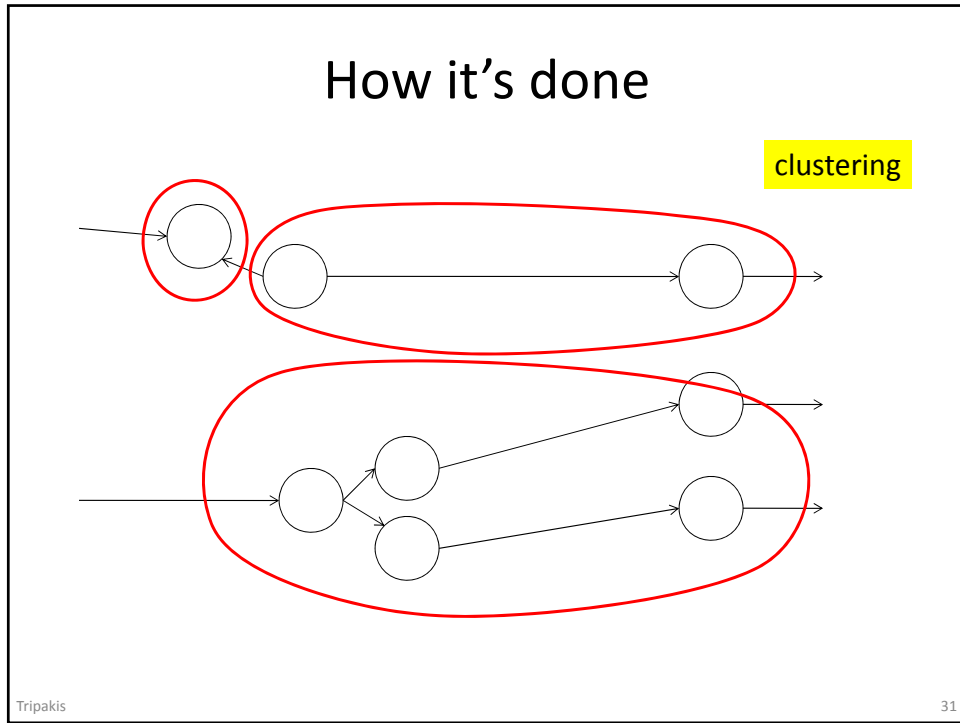
interface

# How it's done



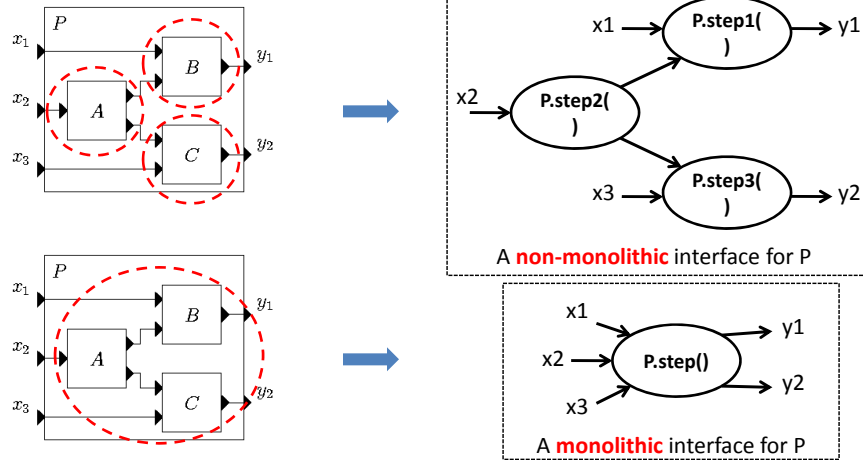
# How it's done







## Different clusterings => different interfaces



trade-off: interface size vs. reusability

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## Different clustering algorithms = different tradeoffs

Clustering method	Complexity	Achieves maximal reusability?	Achieves minimal interf. size?	Modularity bound?	Achieves minimal code size?
"step-get"	Polynomial	No	Almost	$\leq 2$ functions	Yes
"dynamic"	Polynomial	Yes	Yes	$\leq N+1$ functions*	No
"disjoint"	NP-complete	Yes	Yes	?	Yes
"greedy"	Polynomial	Yes	No	?	Yes

\*  $N$  = number of block outputs

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[RTAS'08]

## Achieving compositionality in **multi-periodic** diagrams

Period of block A = 3

Interface enriched with  
deterministic unary automata

*What is the period of P?*  
 $GCD(3,2) = 1$  : over-approximation  
 too conservative

Tripakis 35

[ACM TECS 2013]

## Non-compositionality of dataflow models (SDF)

FIFO queue

**monolithic interface**

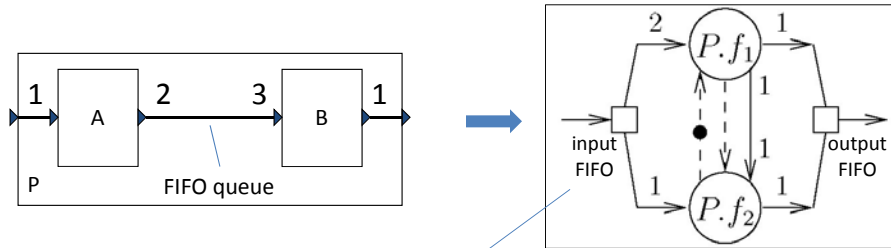
**original model does not deadlock**

**monolithic interface  
=> deadlocks!**

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[ACM TECS 2013]

# Interfaces for dataflow models



SDF++ :

- shared FIFOs
- but deterministic!

**Non-monolithic interface for  $P$**   
(generated automatically)

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# Incremental design using refinement theories

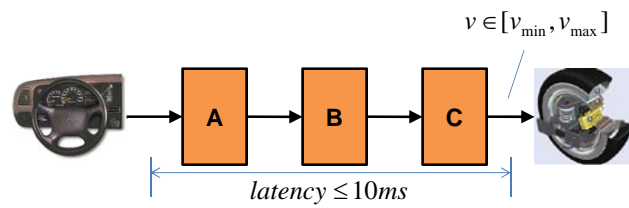
Joint work with B. Lickly, E. Lee, M. Geilen, M. Wiggers, C. Stergiou (Berkeley), T. Henzinger (IST Austria), M. Broy (TU Munich), V. Preteasa, I. Dragomir (Aalto)

Part of NSF Project COSMOI



## Incremental design

Suppose we have designed and verified this “steer-by-wire” system:

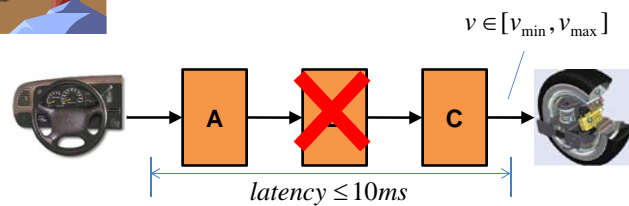


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## Incremental design

Suppose we want to replace B with Z:



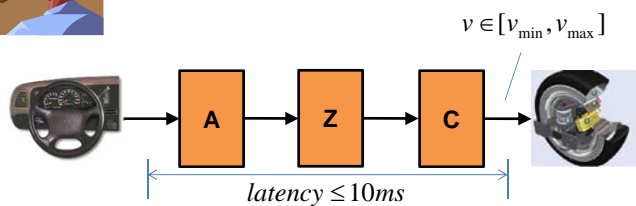
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# Incremental design

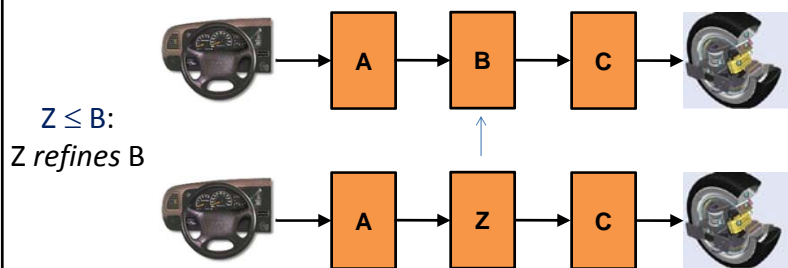


How to ensure properties are preserved  
**(substitutability)?**



# Refinement theories

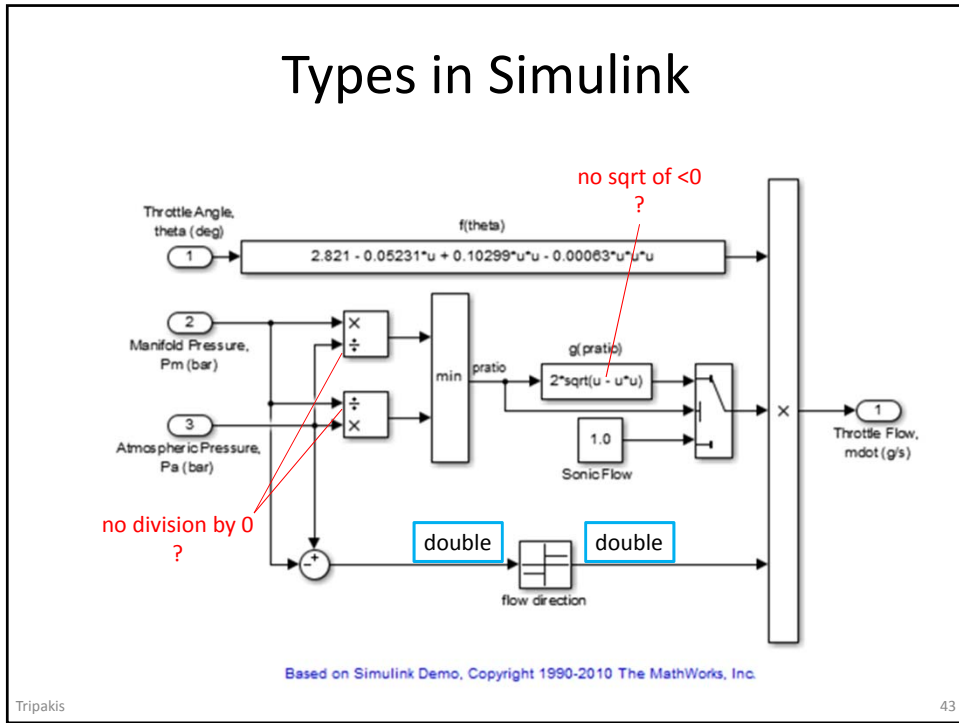
(behavioral type theories)



- (1) If  $A' \leq A$  and  $A$  satisfies  $P$  then  $A'$  satisfies  $P$ .
- (2) If  $A' \leq A$  and  $B' \leq B$ , then  $A' \cdot B' \leq A \cdot B$ .

$Z \leq B$  and (1) and (2)  $\Rightarrow$  **substitutability!**

# Types in Simulink

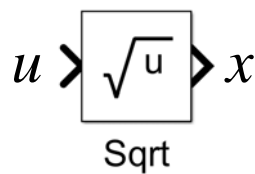


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[ACM TOPLAS 2011]

# Relational interfaces



`double -> double`

standard type

$$u \geq 0 \wedge x = \sqrt{u}$$

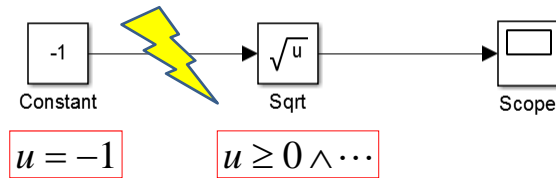
relational interface

Can describe systems which are both **non-deterministic** and **non-input-receptive**

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## Catching incompatibility using symbolic methods

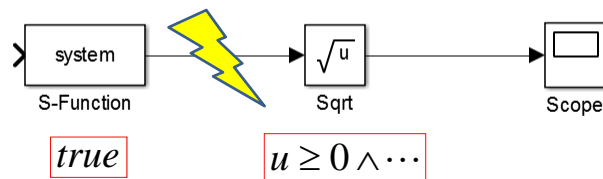


caught by taking the conjunction of the two formulas  
=> use SAT/SMT solvers to check for satisfiability

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## A more tricky example

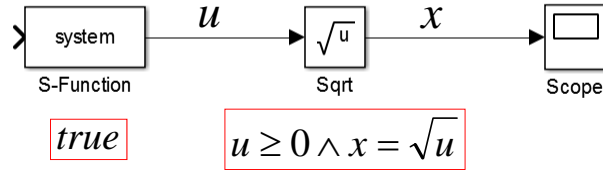


still incompatible, but not just conjunction of formulas

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## Serial composition involves $\forall - \exists$ quantification

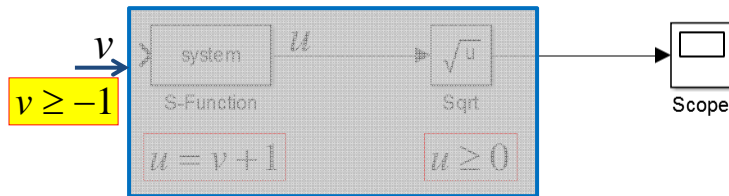


$$\forall u: (true \Rightarrow \exists x: u \geq 0 \wedge x = \sqrt{u}) \equiv false$$

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## Inferring new constraints on inputs (interface synthesis)



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## Refinement relation

$$\phi' \leq \phi \stackrel{\text{def}}{=} \begin{array}{l} in(\phi) \Rightarrow in(\phi') \\ (in(\phi) \wedge \phi') \Rightarrow \phi \end{array}$$

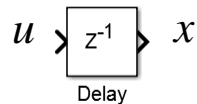
$$in(\phi) \stackrel{\text{def}}{=} \exists \text{outputs: } \phi$$

- Refinement  $\Leftrightarrow$  substitutability:  
 **$A'$  can replace  $A$  in any context iff  $A' \leq A$ .**
  - i.e., refinement **both necessary and sufficient** condition for substitutability.
  - Note: sometimes the strongest contravariance condition  $\phi' \Rightarrow \phi$  is used, which is sufficient but not necessary.

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## Handling components with state



$$x = s \wedge s' = u$$

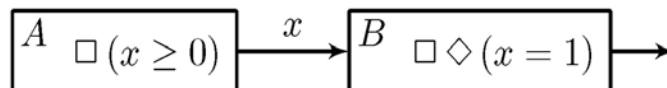
$s$  : state variable

$s'$  : next state variable

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## Expressing temporal properties in LTL (including liveness)



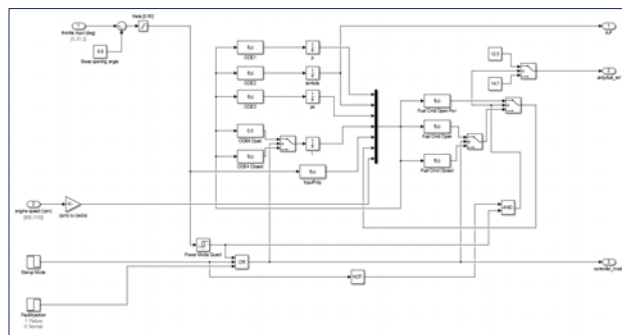
Tripakis

[EMSOFT 2014]

## Latest work (in arxiv.org)

- Prototype tool:
  - Input: Simulink models
  - Theory implemented in Isabelle theorem prover
  - Translate Simulink model to relational interfaces and use Isabelle to automatically check and simplify formulas
- Case study: fuel control system by Toyota (3-level hierarchy, 70 blocks)

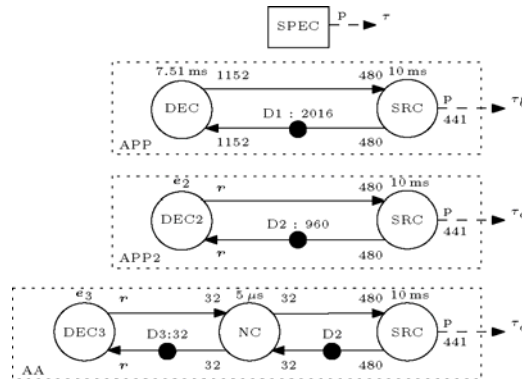
Sample subsystem  
of the FCS model



Tripakis

## Another refinement theory for real-time properties: **throughput** and end-to-end **latency**

- Components described as dataflow models, timed automata, RTC, ...
- Refinement relation: “the earlier the better”
- Worst-case throughput and latency (lower & upper bounds) preserved by refinement
- Under certain monotonicity assumptions (which don’t always hold in real-life?)



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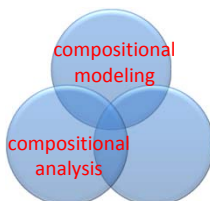
[HSCC 2011]

Skip

## Multi-view modeling

Joint work with J. Reineke (Saarland) and C. Stergiou (ex-Berkeley&Penn, now Google)

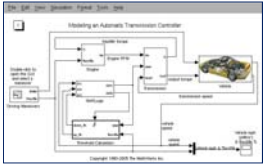
Part of NSF Project COSMOI




## Multi-view modeling

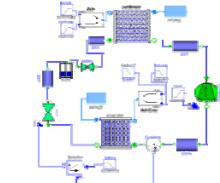
Complex systems => different stakeholders => different concerns

Multi-view modeling: model different **aspects** of the system (e.g., functionality, performance, energy, cost, ...)

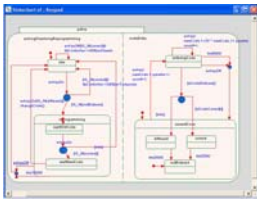


Low-level controllers  
Simulink





Physical dynamics  
Modelica

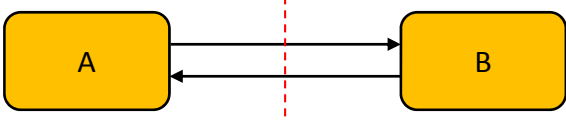


Supervisory  
controllers  
Rhapsody/  
SysML


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## From (separate) components to (overlapping) views

- Composition theories tell us how to compose interacting but **separate** components:



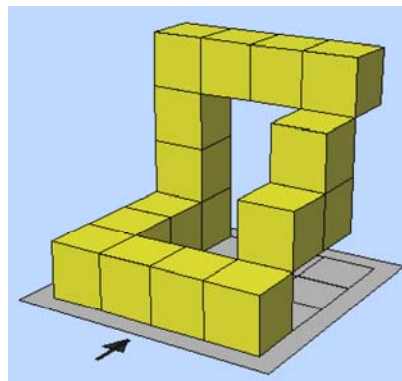
- Multi-view modeling**
  - Views model **overlapping** aspects



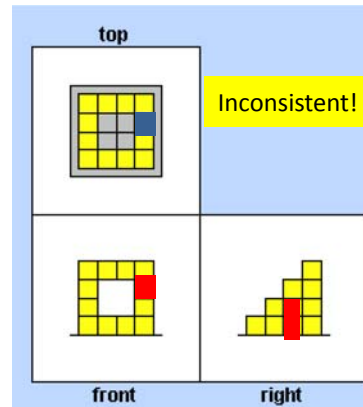
Problem: how to guarantee view consistency?

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## Example: Geometric (Static) Views



3D structure



2D views

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## Our work

[TACAS 2014,  
Journal paper  
under prep.]

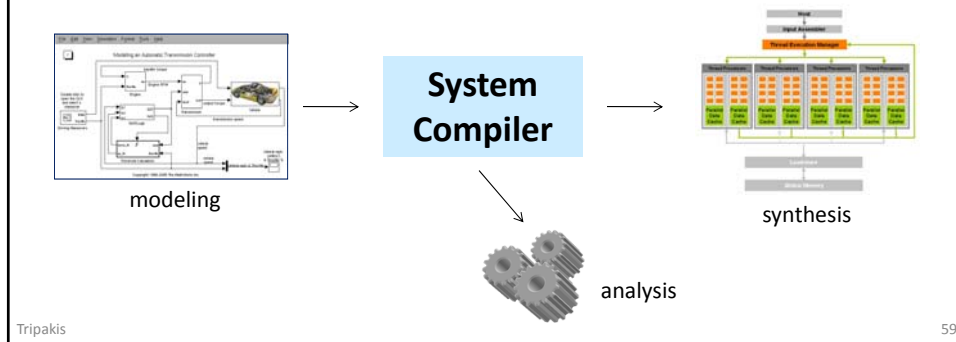
- **Dynamical** systems and views: languages, automata, symbolic transition systems, ...
- Formalization of view **consistency** and other problems of multiview modeling
- Algorithms to solve these problems in concrete settings: finite automata, symbolic transition systems, ...

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## Conclusions

- Model-based system design: key for the “smart societies” of the future, and **more than a buzzword!**
  - How to build “system compilers”
- **Compositionality**: key for complex system modeling, analysis, and implementation



## Thank you

- Questions?

